

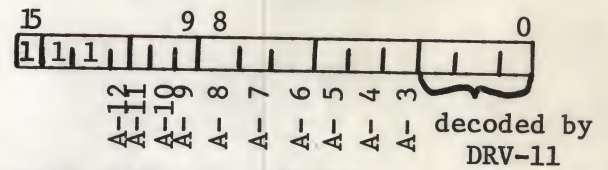
DRV-11 Configuration Guide (M7941)

I= Installed jumpers=0

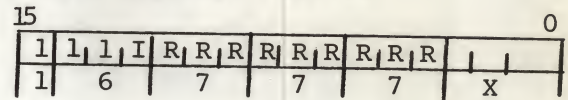
R= Removed jumpers=1

1.

Address Selection

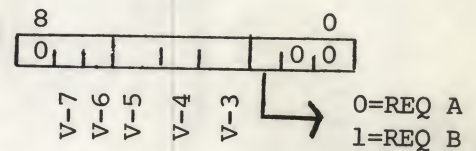


Standard address: 16777X

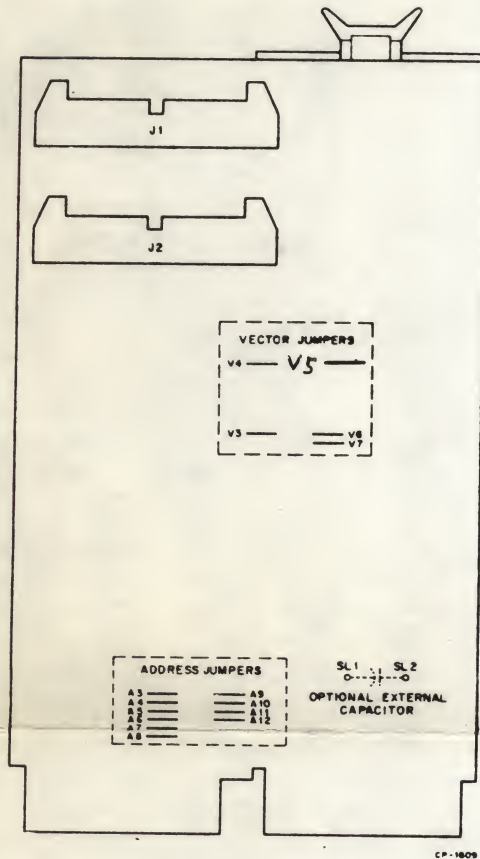
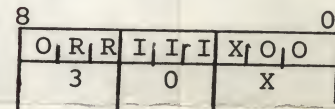


2.

Vector Selection



Standard Vector: 300



3.

External Capacitor

Used to modify the pulse width of New Data Ready pulse and Data Transmitted Pulse (ref. Microcomputer Handbook). Note that the trailing edge of one of these two pulses must be used to clear interrupt requests.

Capacitor (ref.)	Pulse Width	
	New Data Ready	Data Transmitted
None	350 ns	1150
.0047 μ F	750 ns	1550
.01 μ F	1550 ns	2400
.02 μ F	2330 ns	3200
.03 μ F	3150 ns	3900

